Docket No.: GR 00 P 1667

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

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WOLFGANG NIKUTTA

Filed

Concurrently herewith

Title

CIRCUIT CONFIGURATION FOR RECEIVING AT LEAST TWO

DIGITAL SIGNALS

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks, Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

German Patent No. 195 10 038 C1 (Vuksic), dated August 14, 1996, pertains to an arrangement for the configuration for auto-calibrating the clock distribution in synchronous digital circuits;

Published International Application No. WO 98/58449 (Arkin), dated December 23, 1998;

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,

For Applicant

WERNER H. STEMER REG. NO. 34,956

Date: April 4, 2001

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FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: GR 00 P 1667 Appl. No. Applicant Wolfgang Nikutta Filing Date April 4, 2001 Group Art Unit				/826234
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EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILI DA	
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ОТН	1	DCUMENTS (Incli	uding Aut	hor, Title, Date, Pe	ertinent Pa	ages, etc)	
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